

ABSTRACT OF THE DISCLOSURE

5 In a semiconductor integrated circuit device, a plurality of tristate buffers provided corresponding to a plurality of internal signals respectively and receiving at its input node the corresponding internal signal are arranged distributed along the direction of extension of a tristate bus, and any of the plurality of tristate buffers is selectively activated, so that a desired internal signal is externally taken. Therefore, lines for taking the internal signals are not concentrated, and therefore a small circuit area is sufficient.

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